



Z i L O G

UP000701-1200

MUZE

PRODUCT UPDATE FOR MUZE PRODUCT SPECIFICATION

INTRODUCTION

The following precautions apply to all MUZE family of devices. The top mark devices include:

Z86E122SZ016SC	Z86E132SZ016SC	Z86E142FZ016SC
Z86E123SZ016SC	Z86E133SZ016SC	Z86E143FZ016SC
Z86E124SZ016SC	Z86E134SZ016SC	Z86E144FZ016SC
Z86E125SZ016SC	Z86E135SZ016SC	Z86E145FZ016SC
Z86E126SZ016SC	Z86E136SZ016SC	Z86E146FZ016SC
Z86E122SZ016EC	Z86E132SZ016EC	Z86E142FZ016EC
Z86E123SZ016EC	Z86E133SZ016EC	Z86E143FZ016EC
Z86E124SZ016EC	Z86E134SZ016EC	Z86E144FZ016EC
Z86E125SZ016EC	Z86E135SZ016EC	Z86E145FZ016EC
Z86E126SZ016EC	Z86E136SZ016EC	Z86E146FZ016EC
Z86E122PZ016SC	Z86E132PZ016SC	Z86E142PZ016SC
Z86E123PZ016SC	Z86E133PZ016SC	Z86E143PZ016SC
Z86E124PZ016SC	Z86E134PZ016SC	Z86E144PZ016SC
Z86E125PZ016SC	Z86E135PZ016SC	Z86E145PZ016SC
Z86E126PZ016SC	Z86E136PZ016SC	Z86E146PZ016SC
Z86E122PZ016EC	Z86E132PZ016EC	Z86E142PZ016EC
Z86E123PZ016EC	Z86E133PZ016EC	Z86E143PZ016EC
Z86E124PZ016EC	Z86E134PZ016EC	Z86E144PZ016EC
Z86E125PZ016EC	Z86E135PZ016EC	Z86E145PZ016EC
Z86E126PZ016EC	Z86E136PZ016EC	Z86E146PZ016EC

DISCUSSION

Solutions To Common Problems

Following are solutions to two problems users have experienced with the Universal Asynchronous Receiver/Transmitter function of the MUZE microcontroller.

UART Problem #1

To clear the RECEIVE error bits reported in the STAT register (08h/Bank Ah), clear the EFR bit in the CNTLA register to 0. RECEIVE must first be disabled (by writing the CNTLA register 03h/Bank Ah, bit 3); otherwise, the EFR bit reset yields no effect.

Solution/Workaround #1A. Use the ZiLOG-supplied [UART driver](#).

Solution/Workaround #1B. Disable the receiver (CNTLA register bit 6, Bank Ah) then clear the EFR bit (CNTLA register bit 3, Bank Ah). Then, reenable the receiver.

UART Problem #2

If both TIE and RIE are set to 1, a receive interrupt is not generated on the incoming (RDR) data.

Solution/Workaround #2A. Use the ZiLOG-supplied [UART driver](#).

Solution/Workaround #2B. The user must set only RIE to 1 and TIE to 0 if Rx interrupts are required.

ADDITIONAL INFORMATION

The following document provides additional information about the MUZE family devices:

- [MUZE Product Specification](#) PS004005-1100

NOTES

Information Integrity

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